

## 1. BASIC INFORMATION

Course	Computer structure
Degree Program	BSc degree in Computer Science
School	Escuela Politécnica
Year	2025-2026
Credits (ECTS)	6
Credit type	Basic
Language(s)	English / Spanish
Delivery mode	On campus
Semester	S2
Academic year	2025-2026
Coordinating professor	

## 2. PRESENTATION

The course Computer Architecture, part of the Bachelor's Degree in Computer Engineering, offers a comprehensive and progressive overview of the foundations underlying the internal operation of digital systems and microcontrollers. Throughout the course, students will study the principles of design and operation of digital systems, analyzing everything from their most basic components to their integration into complex architectures. The course will delve into the basic units of a microcontroller, exploring their internal organization, modes of operation, and their importance in the development of embedded systems and intelligent electronic devices.

Likewise, the study of different families of microcontrollers will be addressed, with special emphasis on critical aspects such as the functioning and management of cache memory, which directly affects the overall system performance. Programming in assembly language will enable students to gain practical insight into direct interaction with hardware and to understand the execution of instructions at a low level. Finally, the course introduces advanced computer architectures, laying the foundations for the design and optimization of high-performance systems. The knowledge acquired will provide essential skills for both the development of software close to hardware and the design of digital systems.

## 3. LEARNING OUTCOMES

### Knowledge:

KNO02. Know the use and programming of computers, operating systems, databases and software with engineering applications.

KNO03. To know the structure, organization, operation and interconnection of computer systems, the fundamentals of their programming.

### Skills:

SK03. Apply the knowledge of the structure, organization, operation, and interconnection of computer systems, and the fundamentals of their programming to solve engineering problems.

SK07. Know the structure and architecture of computers and their basic components.

## 4. CONTENTS

The Contents of the course/module are listed below:

- Digital systems
- Microcontrollers.
- The elementary units of a microcontroller.
- Microcontroller families: the cache problem.
- Assembler.
- Advanced computer architectures.

## 5. TEACHING-LEARNING METHODOLOGIES

The types of teaching-learning methodologies used are indicated below:

- Lectures/ web conferencing
- Problem-based learning

## 6. LEARNING ACTIVITIES

Listed below are the types of learning activities and the number of hours the student will spend on each one:

**Campus-based mode:**

Learning activity	Number of hours
Lectures	10
Practical seminars	20
Problem-solving	22
Workshops and/or laboratory work	20
Independent working	56
Debates and discussions	8
Tutorials	12
On campus knowledge assessment tests	2
<b>TOTAL</b>	<b>150</b>

## 7. ASSESSMENT

Each assessable learning activity represents an opportunity for the student to make progress, receive feedback, and consolidate knowledge, skills, and competences. The Learning Outcomes outlined in this guide provide direction for this process and serve as benchmarks for their achievement.

Listed below are the assessment systems used and the weight each one carries towards the final course grade:

**Campus-based mode:**

Assessment Systems	Weight (%)
On campus knowledge assessment tests	40-60%
Case/problem	10-30%
Performance assessment	0-15%
Workshops/lab work journal	15-30%

## 7.1. First exam period

In order to pass the course/module in the ordinary call, the student must obtain a grade greater than or equal to 5.0 (out of 10), in all the evaluation systems proposed in this guide. The final grade will be calculated from the weighted average of all the evaluation systems described.

If in any of the evaluation systems proposed in this guide, a grade lower than 5.0 (out of 10) is obtained, the final grade of the course/module will be “fail” even if, in the result of the weighted average, a value higher than 5.0 (out of 10) is obtained. In the latter case, the course/module would still be “failed” obtaining a final grade of 4.0 (out of 10).

### *Delivery of activities*

Compliance with deadlines is essential to ensure the fairness and planning of the training process.

In case of not submitting an evaluable formative activity in due time and form, and without prior justification, it will not be evaluated and, therefore, will be recorded as “not submitted”.

The student is encouraged to communicate with sufficient time in advance to the teacher of the course/module, any difficulty that may affect their participation in any activity.

### *Attendance*

Active participation in the training sessions is a key component of learning. In order to pass the course/module, at least 50% attendance is required. If this minimum percentage is not reached, the teacher may consider the course/module as “failed”, according to the evaluation regulations of the Universidad Europea de Andalucía.

## 7.2. Second exam period

The extraordinary exam offers a new opportunity for students to demonstrate their learning. To pass it, it will be necessary to obtain a final grade (weighted average) equal to or higher than 5.0 (out of 10.0).

### *Delivery of activities*

The student must submit and pass those mandatory training activities not delivered or not passed in the ordinary call, respecting the new deadlines established. In case of failure to comply with these new deadlines, the activity will not be evaluated and, therefore, will be recorded as “not presented”.

## 8. SCHEDULE

This table shows the delivery deadline for each assessable activity in the course:

Assessable activities	Date
Activities Unit 1 (individual)	Week 3
Activities Unit 2 (individual)	Week 6
Activities Unit 3 (individual)	Week 9
Activities Unit 4 (individual)	Week 12
Activities Unit 5 (individual)	Week 13
Activities Unit 5 (teams)	Week 15
Assessment	Week 16

This schedule may be subject to changes for logistical reasons relating to the activities. The student will be notified of any change as and when appropriate.

## 9. REFERENCES

The reference material for the subject area is as follows:

- ATmega328P Technical Manual
- ATmega328P Assembler Manual
- Access to software resources for Atmel programming

A continuación, se indica bibliografía recomendada:

- LLORIS, A., PRIETO, A. (1996). Diseño lógico. Madrid: Mc Graw Hill.
- STALLINGS, W (2015). Computer Organization and Architecture: Designing for Performance. Pearson Education.
- TOCCI, R. (2010). Digital Systems: Principles and Applications. Pearson Education.

## 10. AREA OF GUIDANCE, DIVERSITY AND INCLUSION

The Area of Guidance, Diversity and Inclusion (ODI) offers support to students throughout their university career, with the aim of facilitating their academic and personal development and supporting them in achieving their goals. This Area focuses its work on three Core pillars: the inclusion of students with specific educational support needs, the promotion of universal accessibility in the educational community and the guarantee of equal opportunities for all.

Among the services offered are:

- **Academic accompaniment and monitoring**, through counselling and the development of personalised plans aimed at those who need to improve their academic performance.

- **Attention to diversity**, through the implementation of non-significant curricular adjustments - in methodological and Assessment aspects - for students with specific educational support needs, in order to guarantee equal opportunities.
- **Extracurricular training resources**, aimed at developing personal and professional Competencies that contribute to the integral growth of students.
- **Vocational guidance**, through the provision of tools and advice to those who have concerns about their choice of Degree or are considering a change in their educational path.

Students in need of educational support can contact the Area via the following email address: [orientacioneducativa@universidadeuropea.es](mailto:orientacioneducativa@universidadeuropea.es)

## 11. ONLINE SURVEYS

Participating in the Satisfaction Surveys is an enriching opportunity to contribute to the continuous improvement of the Degree as well as the institution. Thanks to them, it is possible to identify which aspects of academics, teaching staff and the teaching-learning process are working well and which can be further improved.

With the aim of encouraging active participation in the completion of surveys among students, various channels of dissemination have been set up. The surveys are available in the space provided on the Virtual Campus and are also sent by email to facilitate access.

The responses collected allow decisions to be made that have a direct impact on the quality of the learning experience and on the day-to-day life of the university community.